

Fig. 1

(Prior Art)

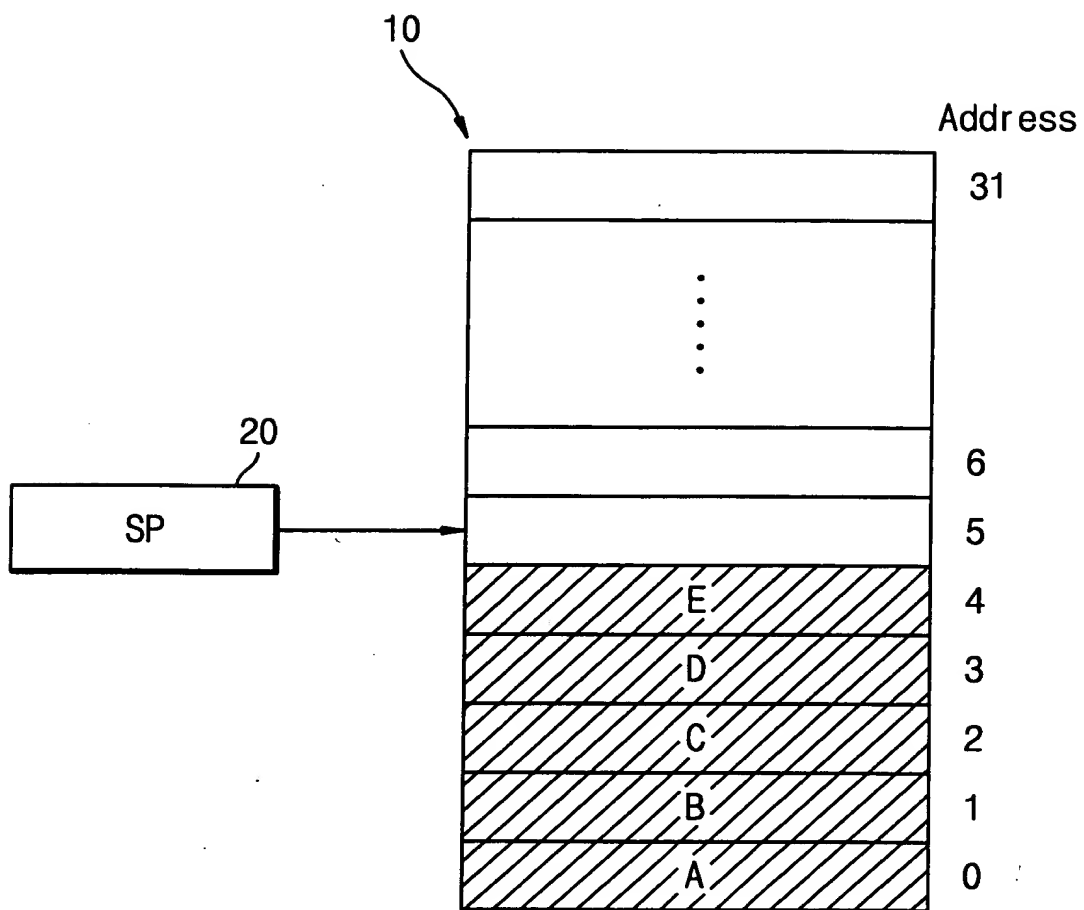


Fig. 2

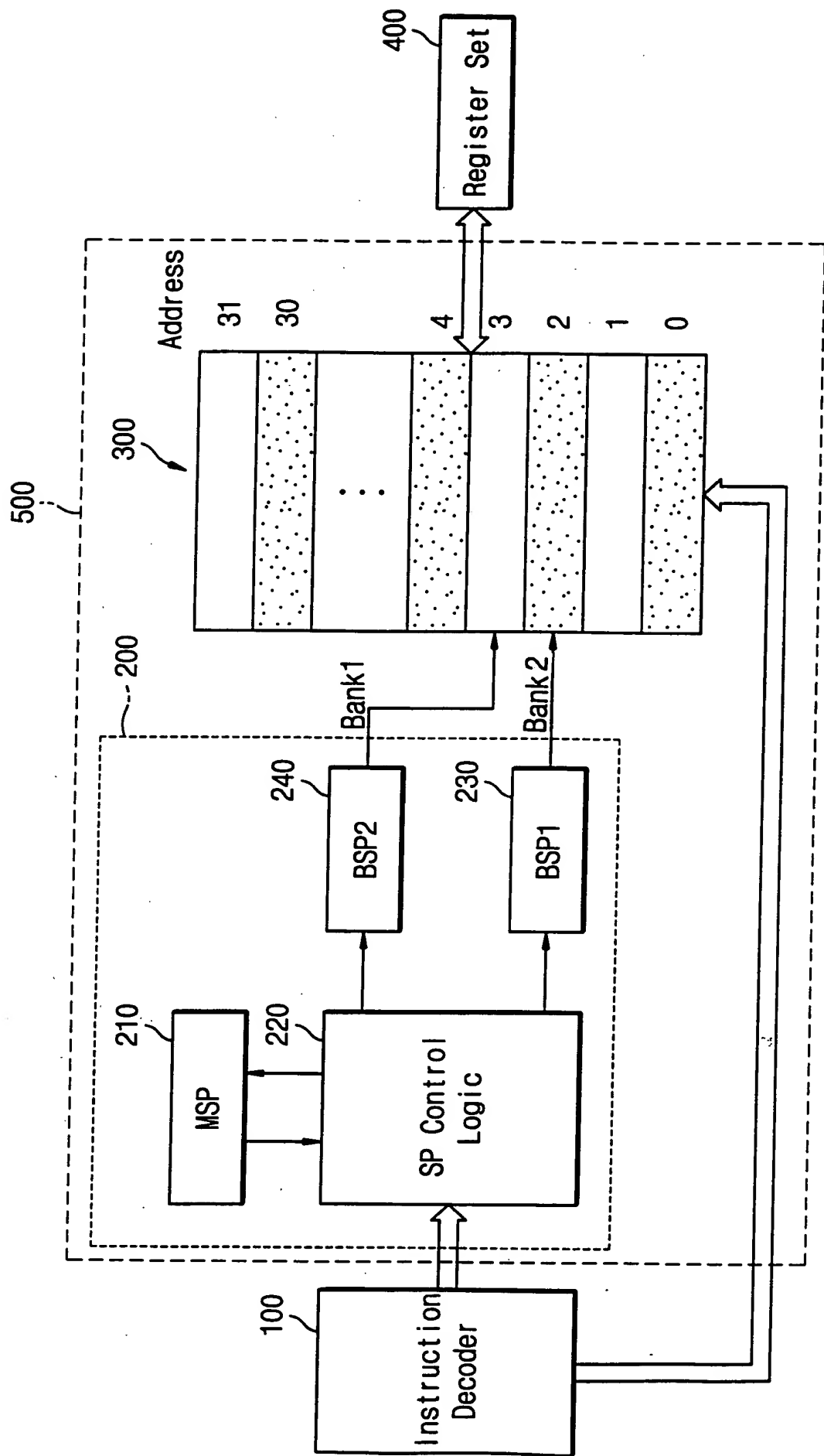


Fig. 3

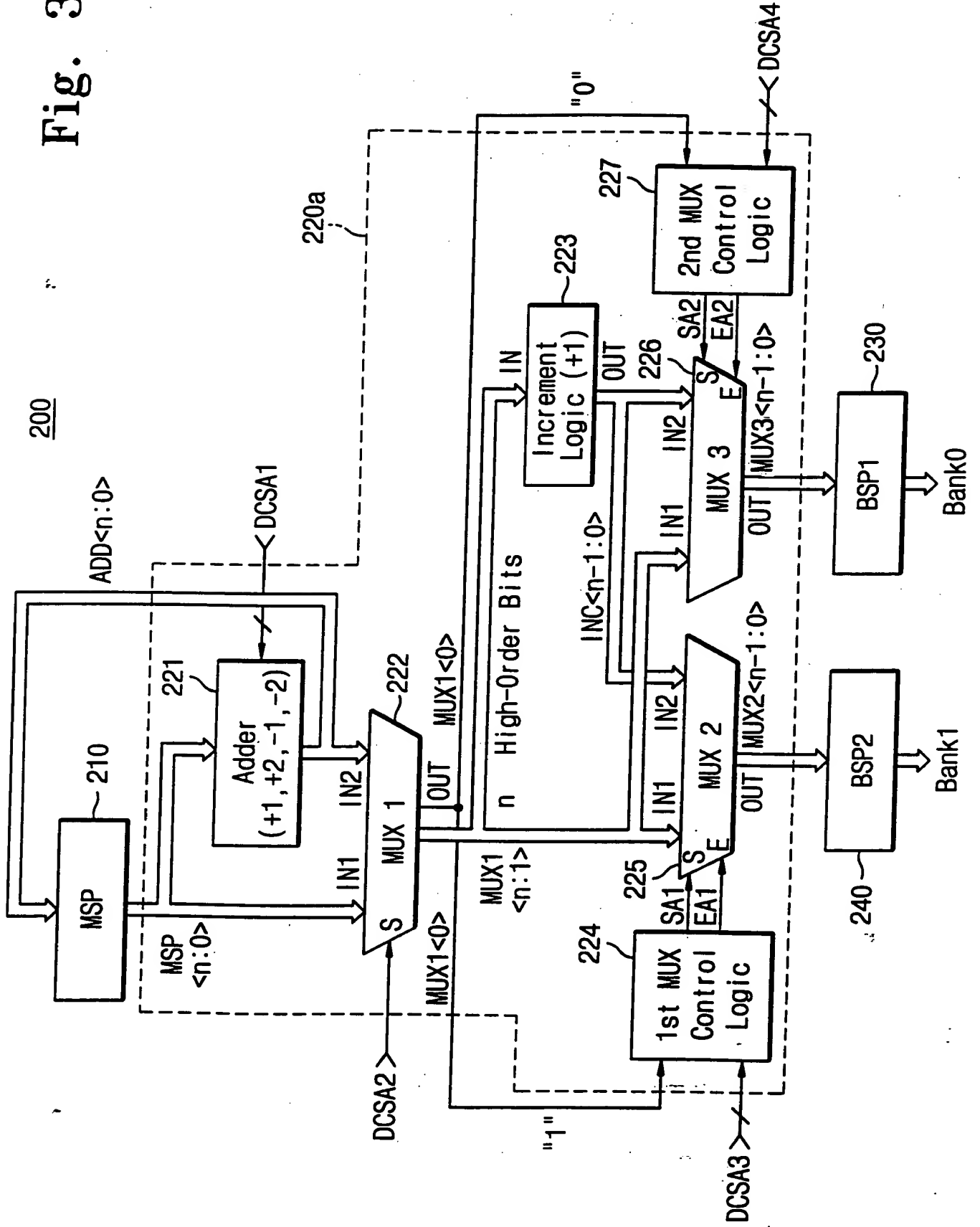


Fig. 4A

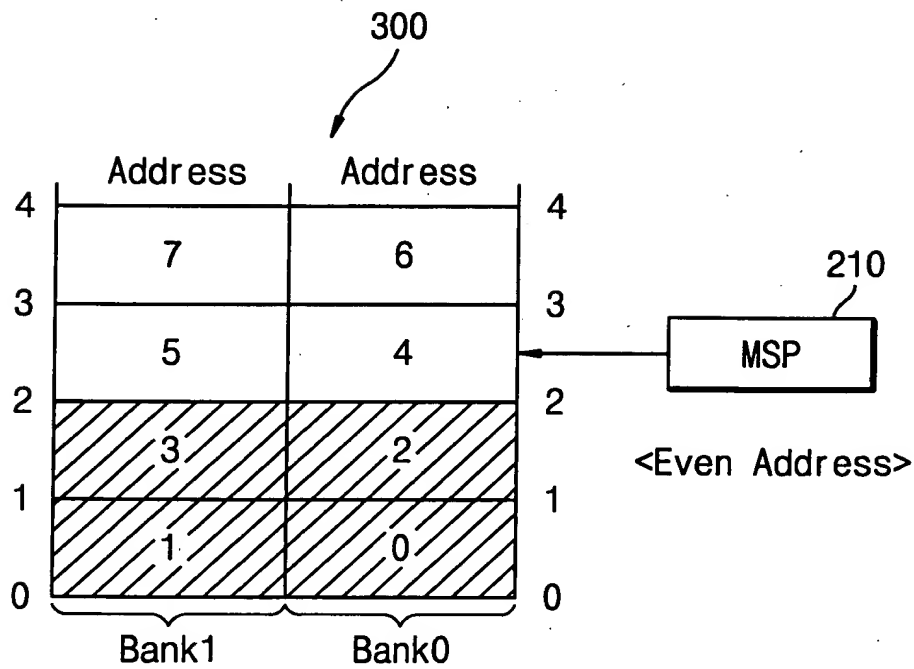


Fig. 4B

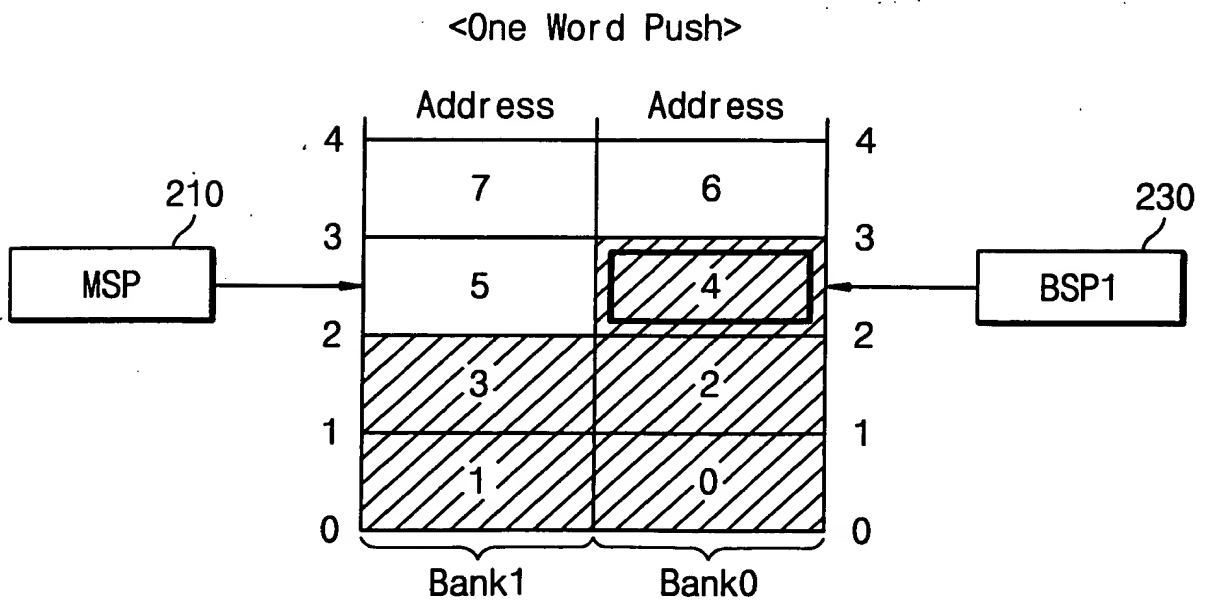


Fig. 4C

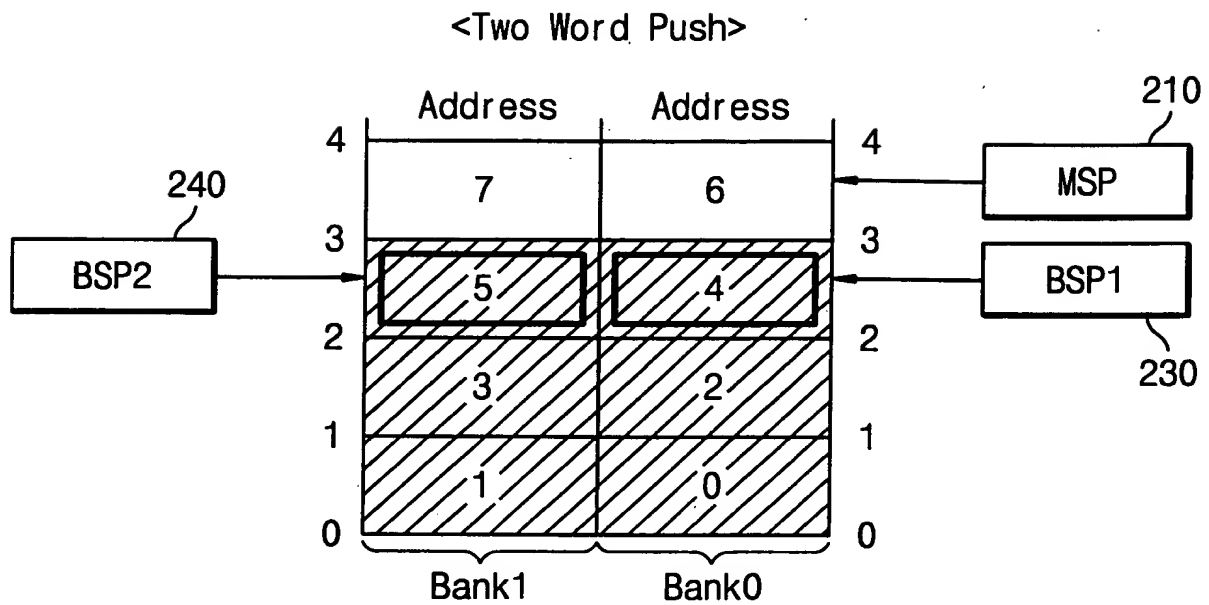


Fig. 4D

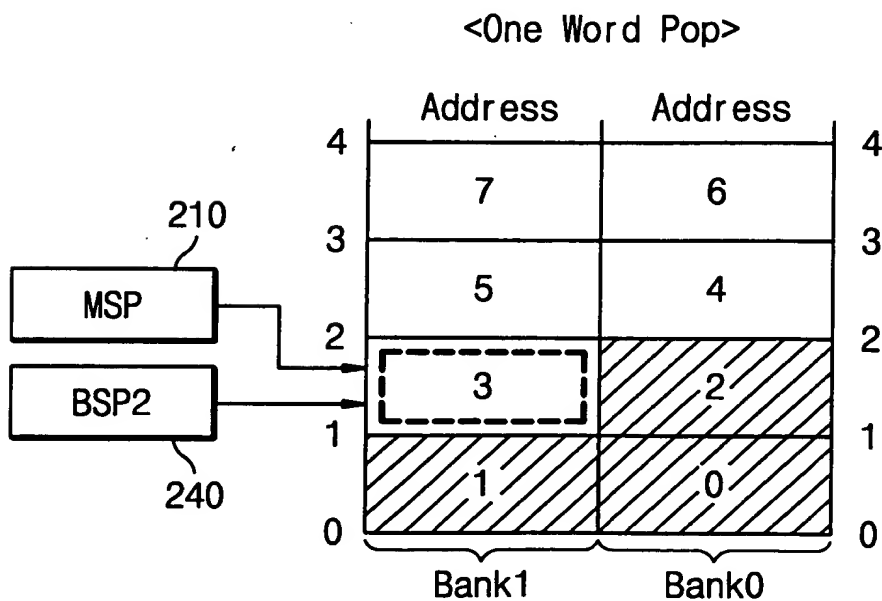


Fig. 4E

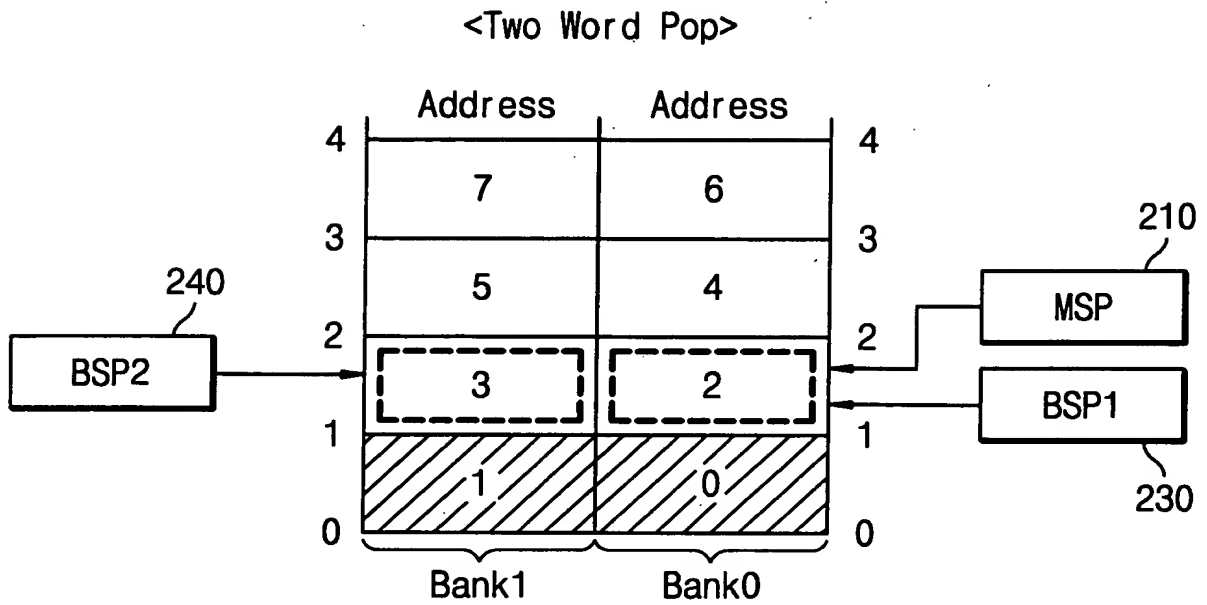


Fig. 5A

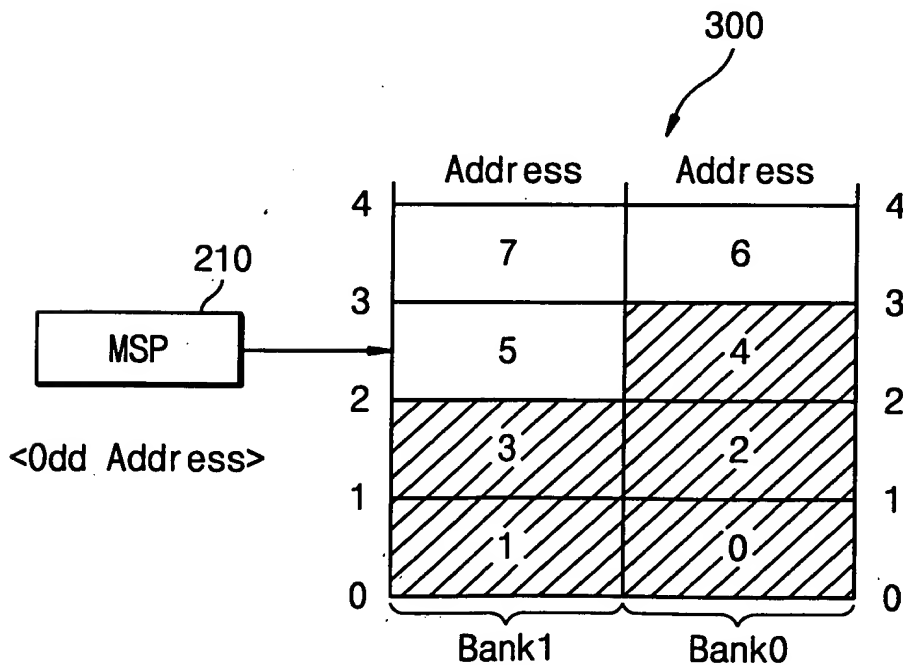


Fig. 5B

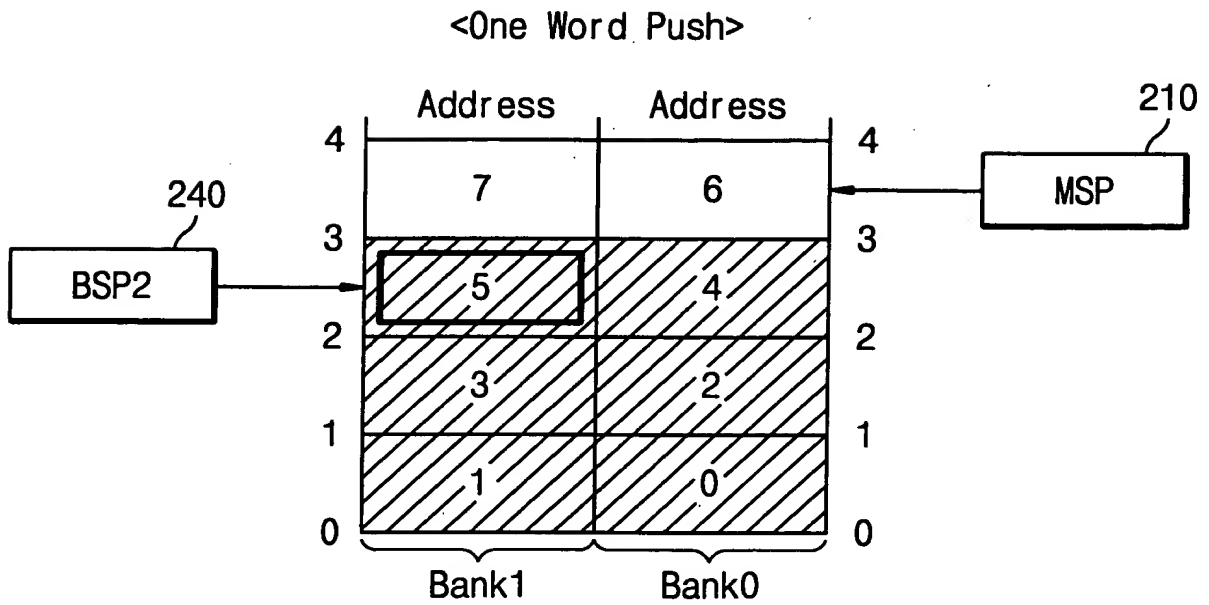


Fig. 5C

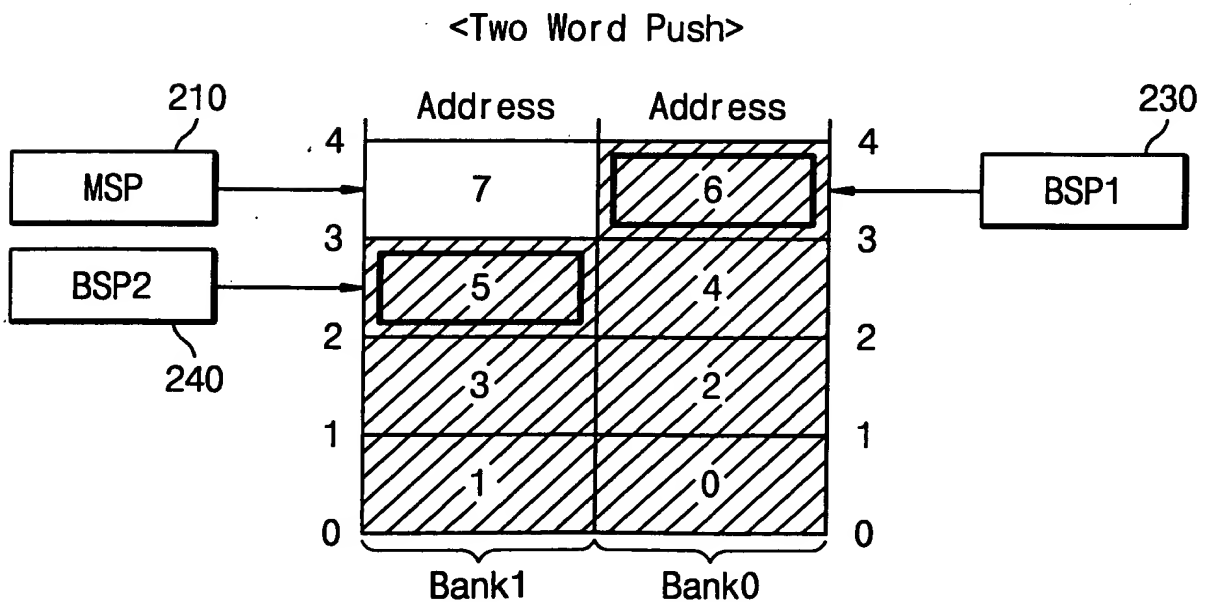


Fig. 5D

<One Word Pop>

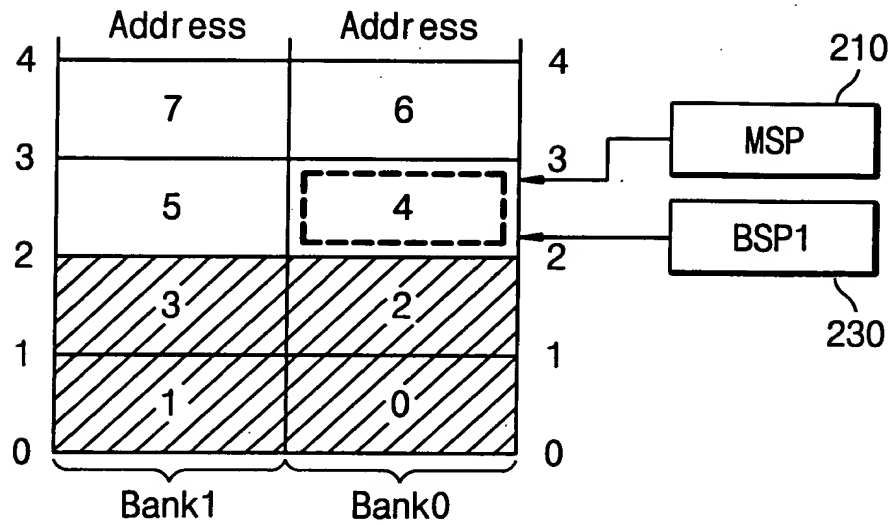


Fig. 5E

<Two Word Pop>

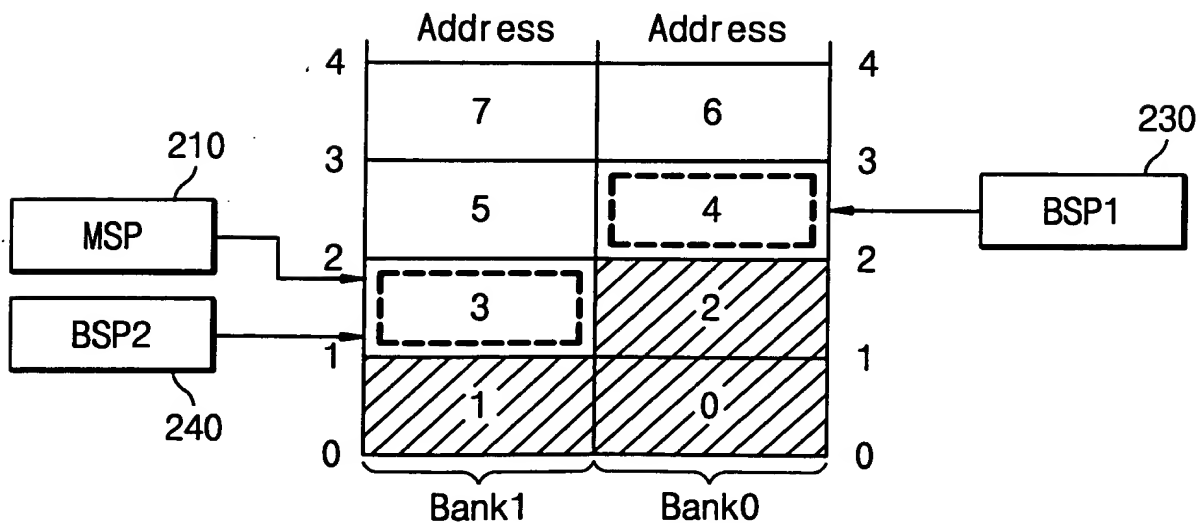


Fig. 6

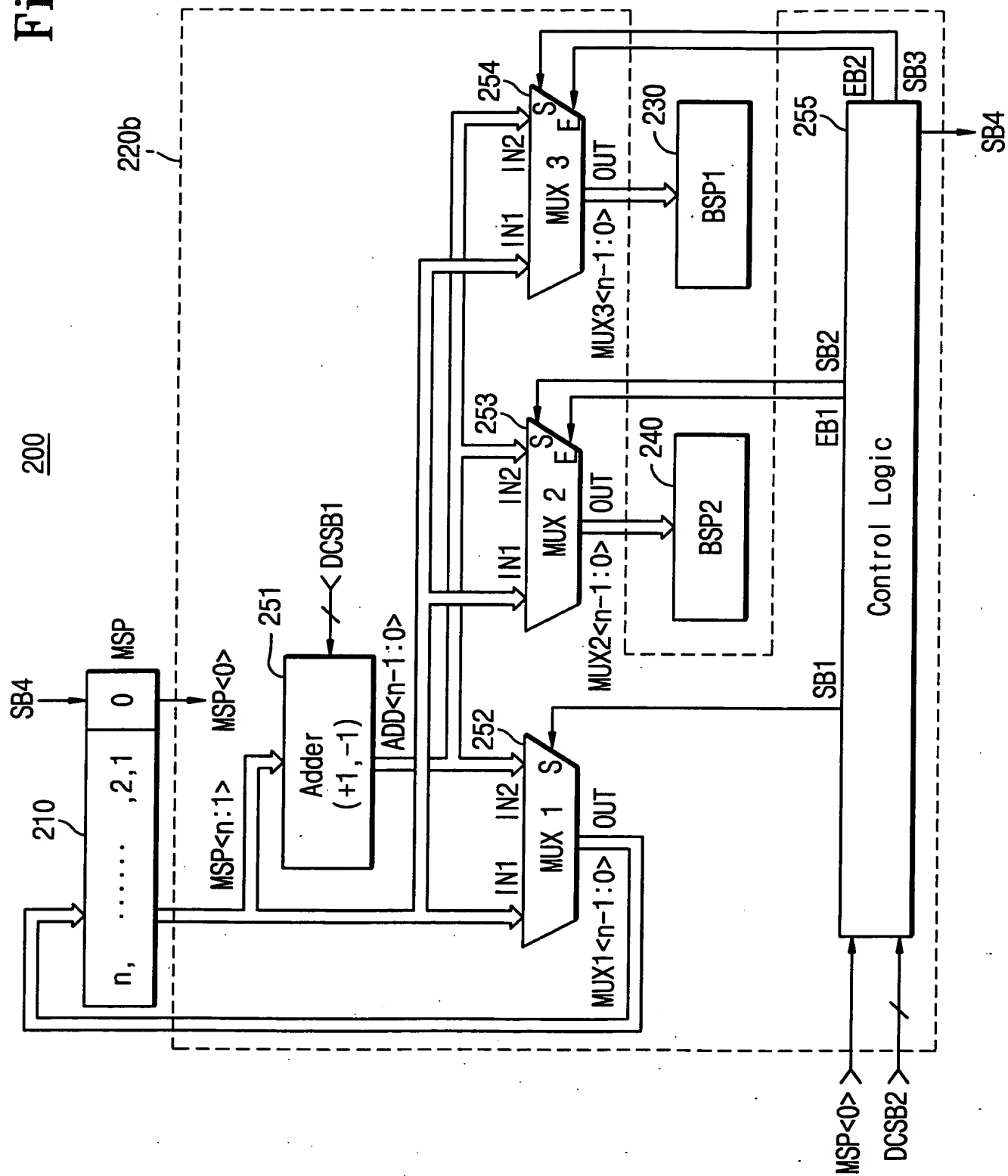


Fig. 7A

<Even Address>

Stack Pointer Instruction	BSP1	BSP2	MSP<n:1>	MSP<0>
1-Word Push	MPS<n:1>	-	MSP<n:1>	Toggled
2-Word Push	MPS<n:1>	MSP<n:1>	MSP<n:1>+1	-
1-Word Pop	-	MSP<n:1>-1	MSP<n:1>-1	Toggled
2-Word Pop	MPS<n:1>-1	MSP<n:1>-1	MSP<n:1>-1	-

Fig. 7B

<Odd Address>

Stack Pointer Instruction	BSP1	BSP2	MSP<n:1>	MSP<0>
1-Word Push	-	MSP<n:1>	MPS<n:1>+1	Toggled
2-Word Push	MPS<n:1>+1	MSP<n:1>	MPS<n:1>+1	-
1-Word Pop	MPS<n:1>	-	MPS<n:1>	Toggled
2-Word Pop	MPS<n:1>	MSP<n:1>-1	MPS<n:1>-1	-